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E. Millet
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

In re Patent Application of
DiStefano et al.

Application No. Unassigned

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For: FAN-OUT SEMICONDUCTOR CHIP
ASSEMBLY

Group Art Unit: Unassigned

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X

Commissioner for Patents
Washington, D.C. 20231



INFORMATION DISCLOSURE STATEMENT

Sir:

It is respectfully requested that the references listed on the enclosed Form PTO-1449 be made of record and considered with respect to the above-referenced U.S. patent application. A copy of each reference was of record in Application No. 08/935,962, the benefit of which is claimed under §120. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

In the event that any fee is due in connection with the present Information Disclosure Statement, the Commissioner is hereby authorized to charge the same to our Deposit Account No. 12-1095.

Respectfully submitted,

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